

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) An interface circuit for supplying a computer logic circuit with first and second inputs having respective first and second predetermined voltage levels that are different from each other and from ground, respectively, the interface circuit comprising:

a first power supply circuit for providing the first input having the first predetermined voltage level in response to a first supply voltage;

a regulator for generating an output having the second predetermined voltage level in response to the first supply voltage; and

a second power supply circuit for providing an output having the second predetermined voltage level in response to a second supply voltage that is different from the first supply voltage and from ground,

wherein said regulator and said second power supply circuit cooperate to provide the second input having the second predetermined voltage level in instances in which the second supply voltage is present, [[, and]]

~~wherein said regulator supplies provides the second input having the second predetermined voltage level in a manner independent of said second power supply circuit in instances in which the second supply voltage is unavailable.~~

2. (Original) An interface circuit according to Claim 1 wherein said regulator provides the second input having the second predetermined voltage level in instances in which the second supply voltage is unavailable.

3. (Original) An interface circuit according to Claim 1 wherein the second input has first and second portions with different tolerances, and wherein said regulator is capable of supplying the first portion of the second input and said second power supply circuit is capable of supplying the second portion of the second input.

4. (Original) An interface circuit according to Claim 3 wherein said second power supply circuit comprises a power switching circuit for providing the output of said second power supply circuit as the second portion of the second input to the computer logic circuit in instances in which the second supply

voltage is present and for providing the output of said regulator as the second portion of the second input to the computer logic circuit in instances in which the second supply voltage is unavailable.

5. (Original) An interface circuit according to Claim 4 wherein said second power supply circuit further comprises a voltage detection circuit for determining if the second supply voltage is present and for correspondingly notifying said power switching circuit.

6. (Original) An interface circuit according to Claim 1 wherein said second power supply circuit comprises an isolation circuit for permitting the second supply voltage to be provided to the computer logic circuit without permitting the computer logic circuit to drive the second supply voltage.

7. (Original) An interface circuit according to Claim 1 wherein the interface circuit and the computer logic circuit are mounted upon an adapter card that is capable of being plugged into an adapter slot to thereby establish electrical contact with at least a first power rail providing the first supply voltage.

8. (Original) An interface circuit for supplying a computer logic circuit with an input having first and second portions that are each at the same predetermined voltage level, the interface circuit comprising:

a regulator for generating an output having the predetermined voltage level in response to a first supply voltage, said regulator providing the output as the first portion of the input to the computer logic circuit; and

a power supply circuit capable of providing an output having the predetermined voltage level in response to a second supply voltage, wherein said second power supply circuit comprises a power switching circuit for providing the output of said power supply circuit as the second portion of the input to the computer logic circuit in instances in which the second supply voltage is present and for providing the output of said regulator as the second portion of the input to the computer logic circuit in instances in which the second supply voltage is unavailable.

9. (Original) An interface circuit according to Claim 8 wherein the output provided by said regulator has a smaller tolerance than the output provided by said power supply circuit.

10. (Original) An interface circuit according to Claim 8 wherein said second power supply circuit further comprises a voltage detection circuit for determining if the second supply voltage is present and for correspondingly notifying said power switching circuit.

11. (Original) An interface circuit according to Claim 8 wherein said second power supply circuit further comprises an isolation circuit for permitting the second supply voltage to be provided to the computer logic circuit without permitting the computer logic circuit to drive the second supply voltage.
12. (Original) An interface circuit according to Claim 8 further comprising another power supply circuit for providing the computer logic circuit with an additional input having a different predetermined voltage level in response to the first supply voltage.
13. (Original) An interface circuit according to Claim 8 wherein the interface circuit and the computer logic circuit are mounted upon an adapter card that is capable of being plugged into an adapter slot to thereby establish electrical contact with at least a first power rail providing the first supply voltage.
14. (Currently amended) A method implemented by an interface circuit for supplying a computer logic circuit with first and second inputs having respective first and second predetermined voltage levels respectively that are different from each other and from ground, the method comprising:
providing the computer logic circuit with the first input having the first predetermined voltage level based upon a first supply voltage;
determining if a second supply voltage that is different from the first supply voltage and from ground is present; and
providing the computer logic circuit with the second input having the second predetermined voltage level,
wherein providing the second input comprises providing the second input having the second predetermined voltage level based upon both the first and second supply voltages if the second supply voltage is present, and
wherein providing the second input comprises providing the second input having the second predetermined voltage level based only upon the first supply voltage if the second supply voltage is unavailable.
15. (Original) A method according to Claim 14 wherein providing the computer logic circuit with the second input in instances in which the second supply voltage is present comprises:
providing a first portion of the second input with a first tolerance based upon the first supply voltage; and

providing a second portion of the second input with a second tolerance based upon the second supply voltage.

16. (Original) A method according to Claim 15 wherein the second portion of the second input is provided with a looser tolerance than the first portion of the second input.

17. (Original) A method according to Claim 14 further comprising isolating the second supply voltage and the computer logic circuit by permitting the second supply voltage to be provided to the computer logic circuit without permitting the computer logic circuit to drive the second supply voltage.